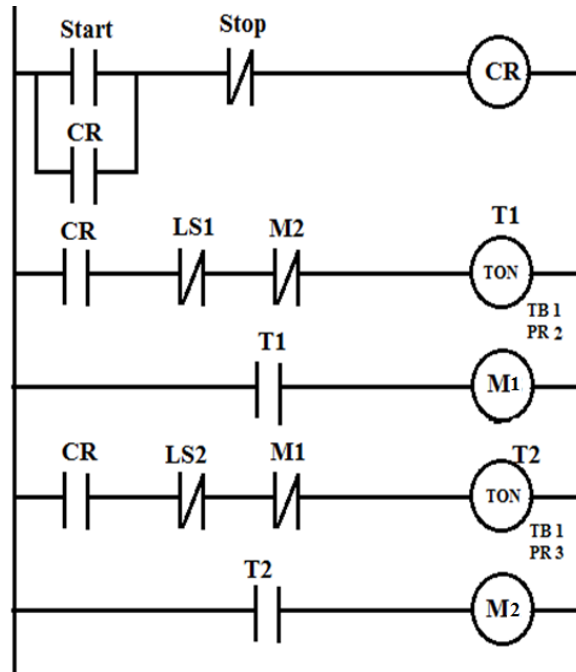


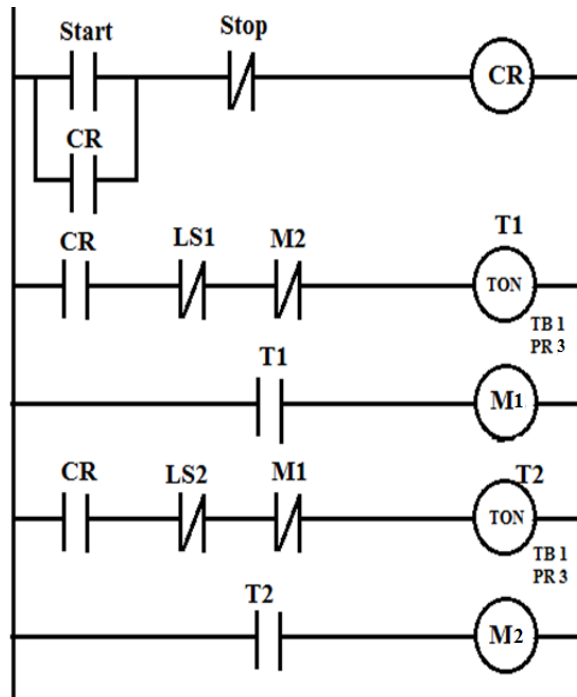
## Report

For the two ladder diagrams shown in Fig.1 and Fig.2, using timing diagram, illustrate the states of CR, T1, T2, M1 and M2 for the states of input switches Start, Stop, LS1 and LS2 that shown in Fig.3

**Fig.1**



**Fig.2**



**Fig.3**

